



SHRI VILEPARLE KELAVANI MANDAL'S  
SHRI BHAGUBHAI MAFATLAL POLYTECHNIC



Question Bank for PT1 – Jan 22 – May 22

Program - CSE  
Course – FMP

Semester – III  
Course code – FMP230805

Ch 1 – The 8086 microprocessor			
	Questions	Answer from	Marks
Q.1	Explain features of 8086 (each 3 mks) min 3 points in feature (each 4 mks) – min 4 points in feature	PPT	3/4
Q.2	Define term Microprocessor - def 2mks , function – 2mks	PPT	4
Q.3	Draw and explain architecture of 8086 Only draw – 3 mks Draw explain any one specific block - 2mks or any two (each for 2 mks )	PPT	4/6/8
Q.4	Define term pipelining and explain any two advantages of pipelining	PPT	4
Q.5	Draw flag register of 8086 and explain (only draw- 2 mks, any flag explanation with example) each 2 mks	PPT	4/6
Q.6	Explain special functions of general purpose registers (AX,BX,CX,DX) 1. Each for 2 mks (minimum 2 functions)...4/6 2. any one ...3mks (minimum 3 functions).. 3 9	PPT	3/4/6
Q.7	Explain use of any pin or signal of 8086 (each for 2 mks)	PPT	4
Q.8	Draw and explain memory bank of 8086. (With all cases)	PPT	6/8
Q.9	Draw and explain memory segmentation of 8086	---	6/8
Q.10	Draw and explain significance of queue in 8086	PPT	6
Q.11	Explain the special and miscellaneous functions of 8086	PPT	4/6
Q.12	Draw functional pin diagram of 8086 only diagram	PPT	4/6
Q.13	Explain BIU and EU of the architecture of 8086		6
Q.14			
Q.15			

Ch 2 – Instruction set & timing diagram of 8086			
	Questions	Answer from	Marks
Q.1	Explain any specific individual / group of instructions for 8086 e.g explain with example PUSH and POP instruction	PPT	4/6/8
Q.2	Explain addressing modes of 8086 (each for 3mks ) 1mk -for explanation 2mk – for example	PPT	4/6/8

Ch 3 – 8086 system bus structure			
	Questions	Answer from	Marks
Q.1	List and explain the use of any one pin of minimum mode Listing – 1mk, explain use 2 mks	PPT	4
Q.2	List and explain the use of any one pin of maximum mode Listing – 1mk, explain use 2 mks	PPT	4



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Q.3	Explain use of any pin or signal minimum or maximum mode (each for 2 mks)	PPT	4/6
Q.4	Explain following methods with the help of diagram 1. Demultiplexing of Address Bus 2. Demultiplexing of Data Bus Each for 3 marks ( 1 mk for diagram , 2 mks for explanation )	PPT	6/8
Q.5	Draw and explain Minimum mode of 8086	PPT	6/8
Q.6	Draw minimum/Maximum mode of 8086	PPT	4/6
Q.7	Draw timing diagram for any one machine cycle of minimum mode of 8086	PPT	4
Q.8	Explain the use of 8288 bus controller in maximum mode of 8086	PPT	4
Q.9	State the function following signals ALE,BHE,DT/R,DEN,IOWR,IORD,MR,MW, HOLD,HOLDA,LOCK	Notes	4/6/8
Q.10			
Q.11			
Q.12			
Q.13			
Q.14			
Q.15			